



S/N 10/728,636

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Thomas P. Griego	Examiner:	Johnathan J. Johnson
Serial No.:	10/728,636	Group Art Unit:	1725
Filed:	December 5, 2003	Docket:	31248-UT-CIP
Title:	Coated and Magnetic Particles and Applications thereof		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 13-4213.

In accordance with the waiver of 37 C.F.R. § 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and publication for all U.S. national patent applications filed after June 30, 2003, only copies of the foreign references and other documents are enclosed.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

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Page 2
Dkt: 31248-UT-CIP

Respectfully submitted,

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Date July 18, 2005

By 
Philip D. Askenazy
Reg. No. 56,721

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 18th day of July, 2005.

Philip D. Askenazy, Reg. No. 56,721
Name

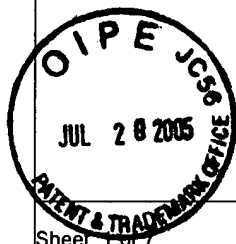
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Complete if Known

Application Number	10/728,636
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First Named Inventor	Griego, Thomas
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Examiner Name	Johnathan J. Johnson

Attorney Docket No: 31248-UT-CIP

US PATENT DOCUMENTS

Examiner Initial *	Cite No. *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code - Number - Kind Code (if known)				

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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Sheet 2 of 7

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		"Back to the Future: Copper Comes of Age", <u>IBM Future Magazine</u> , Number 4, (1997),1-10	
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		CHIDSEY, CHRISTOPHER E., et al., "Chemical Functionality in Self-Assembled Monolayers: Structural and Electrochemical Properties", <u>Langmuir</u> , Vol. 6, No. 3,(1990),682-691	
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		GEWIRTH, ANDREW A., "Scanning-Probe Microscopes in Electrodeposition", <u>The Electrochemical Society Interface</u> , (Spring 1998),1-4	
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		HWANG, SEONG-YONG , et al., "Microstructure of a Lead-Free Composite Solder Produced by an In-Situ Process", <u>Journal of Electronic Materials</u> , Vol. 31, No. 11,(2002),1304-1308	

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		WOLF, STANLEY , "Introduction fo Dual-Damascene Interconnect Processes", <u>Silicon Processing for the VLSI Era - Vol. 4</u> , (2002),674-679	

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		ZOOK, LOIS A., et al., "Density and Solubility of Nafion: Recast, Annealed, and Commercial Films", <u>Analytical Chemistry</u> , Vol. 68, No. 21, (11/1/1996), 3793-3796	

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